



SEMIPACK 3

IGBT M7 Modules

SKM400GB12M7

Features*

- $V_{CE(sat)}$ with positive temperature coefficient
- High overload capability
- Low loss high density IGBT's
- Fast & soft switching inverse CAL diodes
- Large clearance (10 mm) and creepage distances (20 mm)
- Insulated copper baseplate using DBC Technology (Direct Bonded Copper)
- UL recognized, file no. E63532

Typical Applications

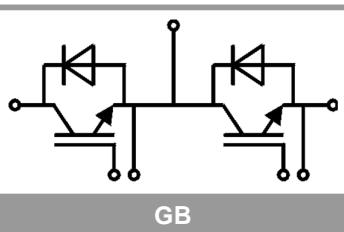
- AC inverter drives
- UPS

Remarks

- Max. case temperature limited to $T_c = T_s = 125^\circ\text{C}$
- Product reliability results are valid for $T_j = 150^\circ\text{C}$ (recommended $T_{j,op} = -40...+150^\circ\text{C}$)
- For storage and case temperature with TIM see document: "Technical Explanations Thermal Interface Materials"

Absolute Maximum Ratings			
Symbol	Conditions	Values	
IGBT			
V_{CES}	$T_j = 25^\circ\text{C}$	1200	V
I_c	$T_j = 175^\circ\text{C}$	$T_c = 25^\circ\text{C}$	555
		$T_c = 80^\circ\text{C}$	424
$I_{C_{nom}}$		400	A
I_{CRM}		800	A
V_{GES}		-20 ... 20	V
t_{psc}	$V_{CC} = 800\text{ V}$ $V_{GE} \leq 15\text{ V}$ $V_{CES} \leq 1200\text{ V}$	$T_j = 150^\circ\text{C}$	8
T_j		-40 ... 175	$^\circ\text{C}$
Inverse diode			
V_{RRM}	$T_j = 25^\circ\text{C}$	1200	V
I_F	$T_j = 175^\circ\text{C}$	$T_c = 25^\circ\text{C}$	440
		$T_c = 80^\circ\text{C}$	329
I_{FRM}		800	A
I_{FSM}	$t_p = 10\text{ ms, sin }180^\circ$, $T_j = 25^\circ\text{C}$	1980	A
T_j		-40 ... 175	$^\circ\text{C}$
Module			
$I_{t(RMS)}$		500	A
T_{stg}	module without TIM	-40 ... 125	$^\circ\text{C}$
V_{isol}	AC sinus 50 Hz, $t = 1\text{ min}$	4000	V

Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
IGBT					
$V_{CE(sat)}$	$I_c = 400\text{ A}$ $V_{GE} = 15\text{ V}$ chiplevel	$T_j = 25^\circ\text{C}$	1.55	1.85	V
		$T_j = 150^\circ\text{C}$	1.80		V
V_{CEO}	chiplevel	$T_j = 25^\circ\text{C}$	0.84	0.90	V
		$T_j = 150^\circ\text{C}$	0.72		V
r_{CE}	$V_{GE} = 15\text{ V}$ chiplevel	$T_j = 25^\circ\text{C}$	1.78	2.4	$\text{m}\Omega$
		$T_j = 150^\circ\text{C}$	2.7		$\text{m}\Omega$
$V_{GE(th)}$	$V_{CE} = 10\text{ V}$, $I_c = 40\text{ mA}$	5.4	6	6.6	V
I_{CES}	$V_{GE} = 0\text{ V}$, $V_{CE} = 1200\text{ V}$, $T_j = 25^\circ\text{C}$			4.0	mA
C_{ies}	$V_{CE} = 10\text{ V}$ $V_{GE} = 0\text{ V}$	$f = 1\text{ MHz}$	84.0		nF
C_{oes}		$f = 1\text{ MHz}$	2.61		nF
C_{res}		$f = 1\text{ MHz}$	1.12		nF
Q_G	$V_{GE} = -8\text{ V} \dots +15\text{ V}$		4000		nC
R_{Gint}	$T_j = 25^\circ\text{C}$		1.5		Ω
$t_{d(on)}$	$V_{CC} = 600\text{ V}$ $I_c = 400\text{ A}$ $V_{GE} = +15/-15\text{ V}$ $R_{G\,on} = 1\text{ }\Omega$ $R_{G\,off} = 1\text{ }\Omega$ $di/dt_{on} = 6000\text{ A}/\mu\text{s}$ $di/dt_{off} = 3350\text{ A}/\mu\text{s}$	$T_j = 150^\circ\text{C}$	320		ns
t_r		$T_j = 150^\circ\text{C}$	66		ns
E_{on}		$T_j = 150^\circ\text{C}$	36		mJ
$t_{d(off)}$		$T_j = 150^\circ\text{C}$	420		ns
t_f		$T_j = 150^\circ\text{C}$	97		ns
E_{off}		$T_j = 150^\circ\text{C}$	48		mJ
$R_{th(j-c)}$	per IGBT		0.091		K/W
$R_{th(c-s)}$	per IGBT, P12 (reference)		0.038		K/W
$R_{th(c-s)}$	per IGBT, HP-PCM		0.027		K/W





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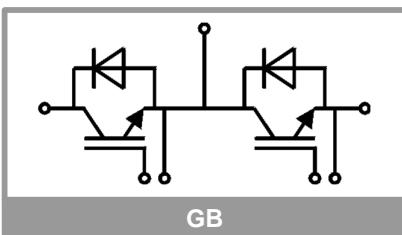
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Characteristics		Symbol	Conditions	min.	typ.	max.	Unit						
Inverse diode													
Inverse diode													
$V_F = V_{EC}$	$I_F = 400 \text{ A}$ $V_{GE} = 0 \text{ V}$ chiplevel	$T_j = 25^\circ\text{C}$		2.20	2.52		V						
		$T_j = 150^\circ\text{C}$		2.14			V						
V_{FO}	chiplevel	$T_j = 25^\circ\text{C}$		1.30	1.50		V						
		$T_j = 150^\circ\text{C}$		0.90			V						
r_F	chiplevel	$T_j = 25^\circ\text{C}$		2.3	2.6		$\text{m}\Omega$						
		$T_j = 150^\circ\text{C}$		3.1			$\text{m}\Omega$						
I_{RRM}	$V_{CC} = 600 \text{ V}$	$T_j = 150^\circ\text{C}$		380			A						
Q_{rr}	$I_F = 400 \text{ A}$	$T_j = 150^\circ\text{C}$		60			μC						
E_{rr}	$V_{GE} = -15 \text{ V}$ $di/dt_{off} = 6650 \text{ A}/\mu\text{s}$	$T_j = 150^\circ\text{C}$		28			mJ						
$R_{th(j-c)}$	per diode				0.14		K/W						
$R_{th(c-s)}$	per diode, P12 (reference)			0.042			K/W						
$R_{th(c-s)}$	per diode, HP-PCM			0.035			K/W						
Module													
L_{CE}				15			nH						
R_{CC+EE}	measured per switch	$T_j = 25^\circ\text{C}$		0.55			$\text{m}\Omega$						
		$T_j = 150^\circ\text{C}$		0.85			$\text{m}\Omega$						
$R_{th(c-s)1}$	calculated without thermal coupling, P12 (reference)			0.0101			K/W						
$R_{th(c-s)2}$	including thermal coupling, T_s underneath module, P12 (reference)			0.015			K/W						
$R_{th(c-s)2}$	including thermal coupling, T_s underneath module, HP-PCM			0.0085			K/W						
M_s	to heat sink M6			3	5		Nm						
M_t		to terminal M6		2.5	5		Nm						
				-			Nm						
w					325		g						



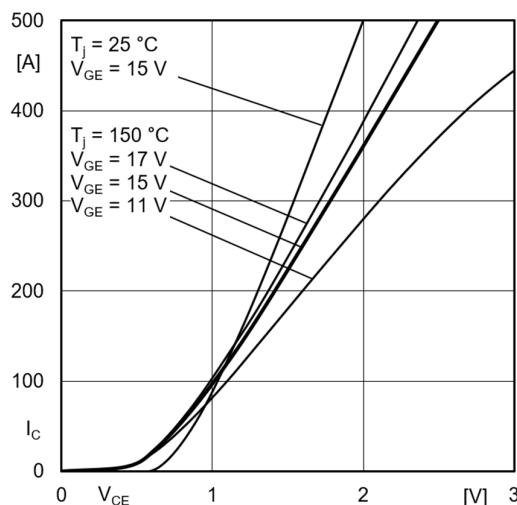


Fig. 1: Typ. output characteristic, inclusive $R_{CC} + EE'$

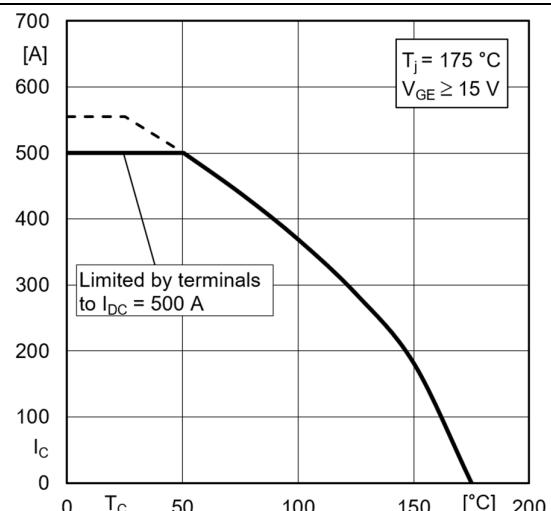


Fig. 2: Rated current vs. temperature $I_c = f (T_c)$

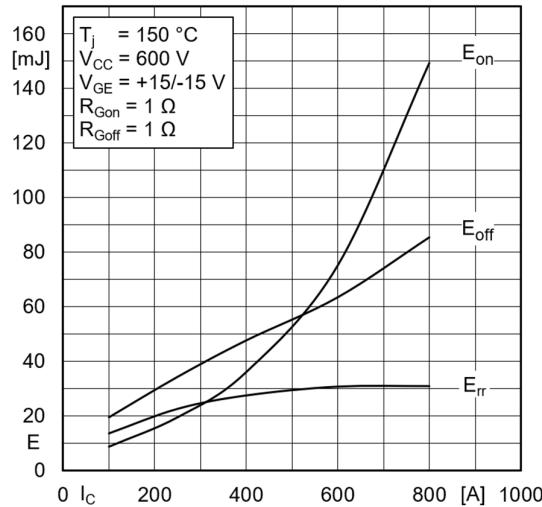


Fig. 3: Typ. turn-on /-off energy = f (Ic)

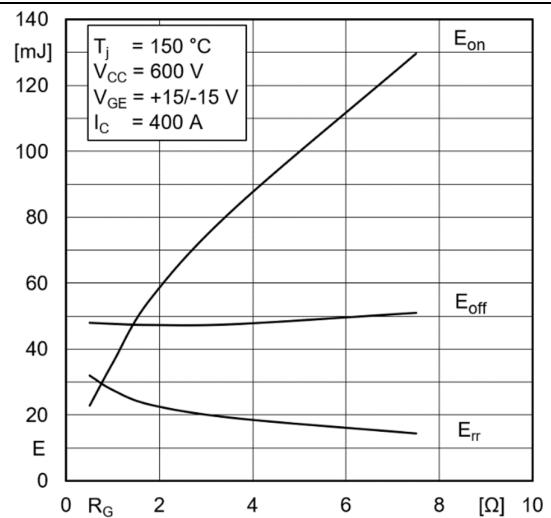


Fig. 4: Typ. turn-on /-off energy = f (Rg)

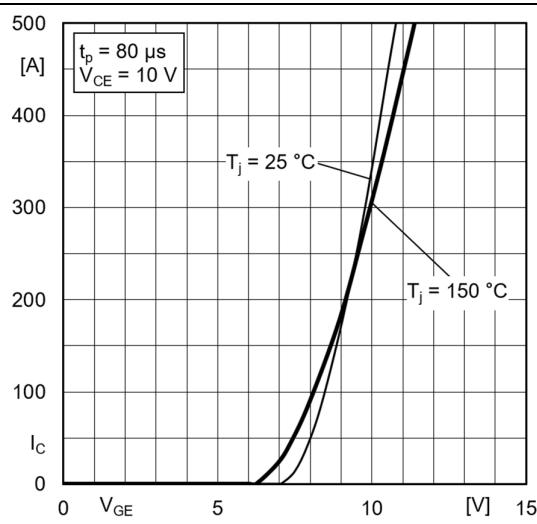


Fig. 5: Typ. transfer characteristic

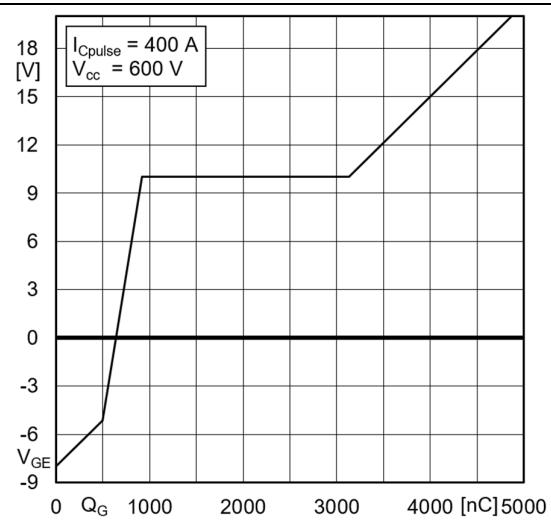


Fig. 6: Typ. gate charge characteristic

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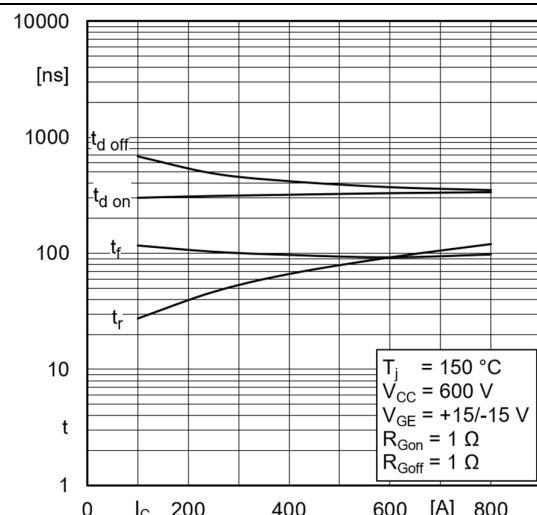


Fig. 7: Typ. switching times vs. I_C

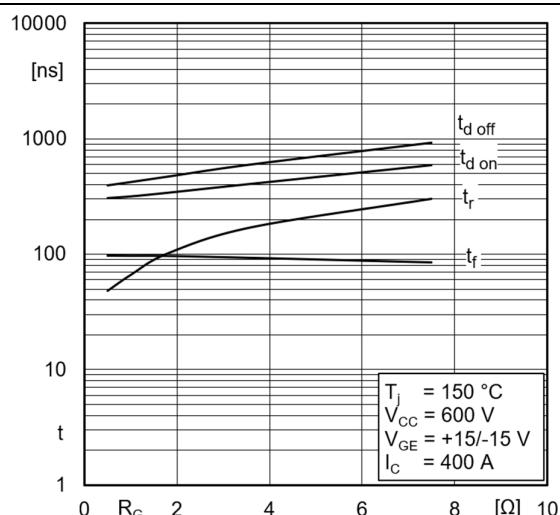


Fig. 8: Typ. switching times vs. gate resistor R_G

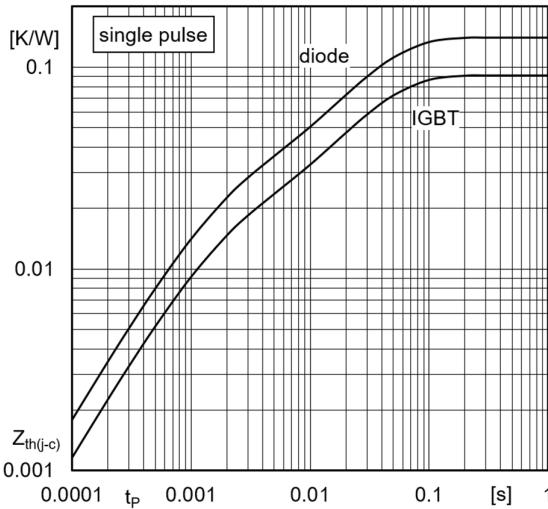


Fig. 9: Transient thermal impedance

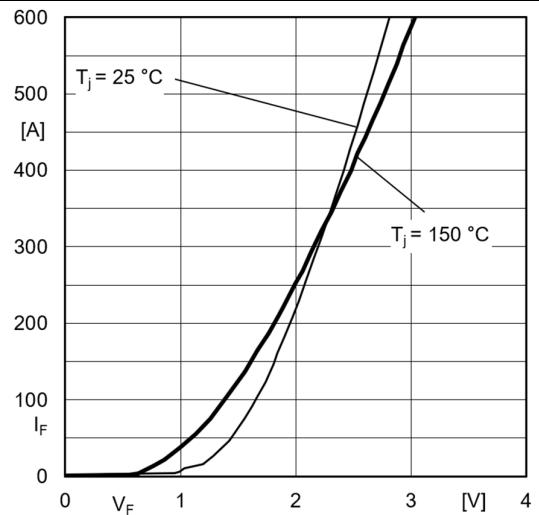


Fig. 10: Typ. CAL diode forward charact., incl. $R_{CC+EE'}$

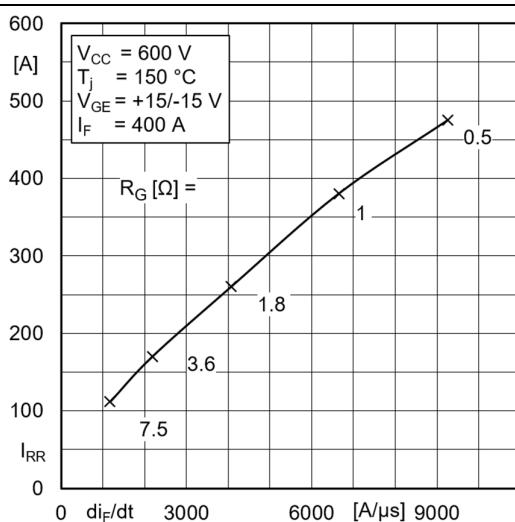


Fig. 11: CAL diode peak reverse recovery current

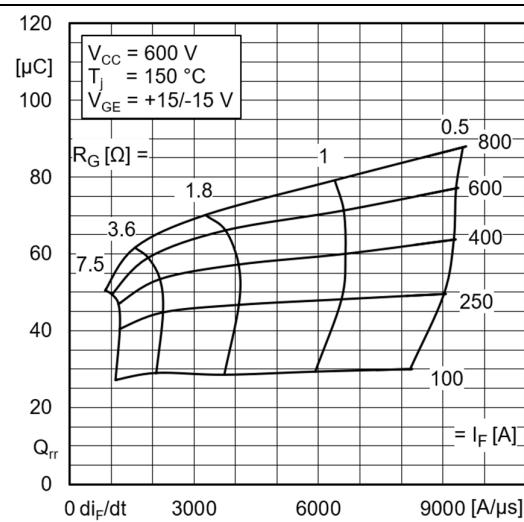
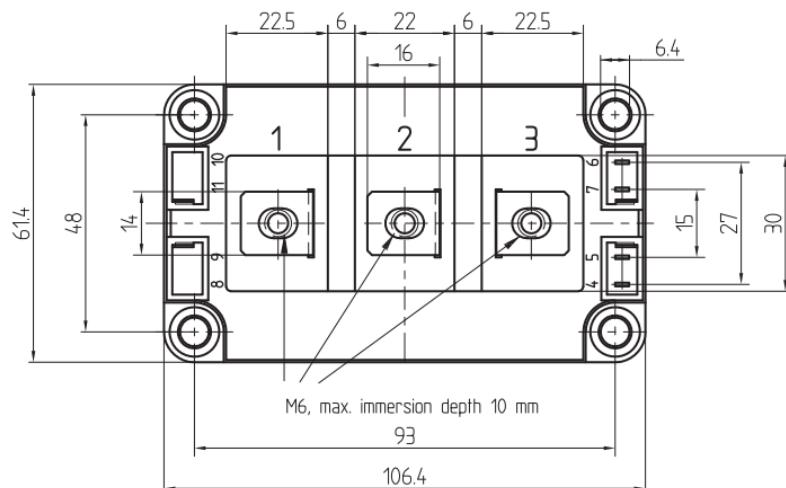
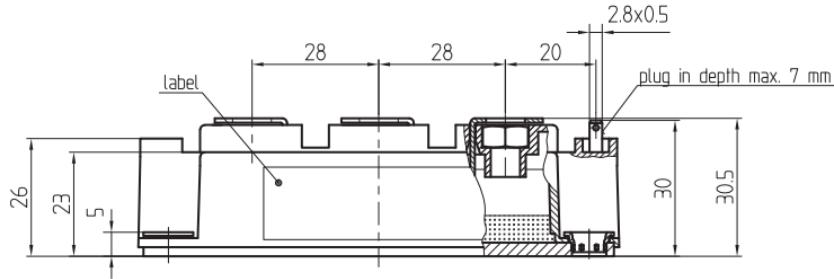


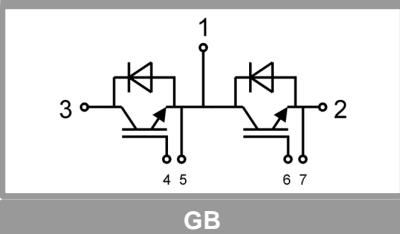
Fig. 12: Typ. CAL diode peak reverse recovery charge

Dimensions in mm



General tolerance +/- 0.5 mm

Pinout and Dimensions



This is an electrostatic discharge sensitive device (ESDS) according to international standard IEC 61340.

*IMPORTANT INFORMATION AND WARNINGS

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